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Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED **AS** 04

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/856924	D F	400 257	738	2826 282	Tran, Tam

APPLICANTS Nobuaki Hashimoto

TITLE Semiconductor device, method of manufacture thereof, circuit board and electronic device

PTO 2040
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drawn 20	Figs. Drawn 21	Print Fig. 5	Total Claims 20	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	TAN RAN 07/03 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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